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## FACSIMILE COVER SHEET

To: Examiner Khiem D. Nguyen	Total Pages Sent: 2
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Facsimile Number: (571) 273-8300	Transmission Date: April 6, 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Tseng Docket No.: TSM03-0763  
Serial No: 10/828,884 Art Unit: 2823  
Date Filed: April 21, 2004  
Title: Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process

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- Certification of Facsimile Transmission (1 page)
- Election (1 page)

Respectfully submitted,

*Kristy Engeldahl*  
Kristy Engeldahl  
Legal Assistant

Confirmation Respectfully Requested

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Art Unit: 2823  
Examiner: Khiem D. Nguyen

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Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
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ELECTION

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed on March 14, 2006, Applicant elects claims 1-19 with traverse for further prosecution in the above patent application. The claims of Group I are directed to a method of forming a semiconductor device, and the claims of Group II are directed to a semiconductor device. Thus, even though Group I is directed to a method and Group II is directed to a device formed by the method, the claims of Group I and Group II are closely related such that examination of both groups would not pose an undue burden on the Examiner.

Respectfully submitted,

6 April 2006  
Date

James C. Kesterson  
James C. Kesterson  
Attorney for Applicant  
Reg. No. 25,882

SLATER & MATSIL, L.L.P.  
17950 Preston Rd.  
Suite 1000  
Dallas, TX 75252-5793  
972-732-1001 - Tel  
972-732-9218 - Fax